ABSTRACT OF THE DISCLOSURE

A bias current supply circuit according to an embodiment of the invention includes a first transistor and a second transistor which form two emitter followers cooperating to supply a base bias current of a bipolar transistor for signal amplification, a normal temperature characteristic circuit which has normal temperature characteristics increasing an amount of current supply with increasing temperature and supplies a base current to the first bipolar transistor, and a reverse temperature characteristic circuit which has reverse temperature characteristics decreasing the amount of current supply with increasing temperature and supplies a base current to the second bipolar transistor.

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